

**Amendments to the Specification**

Please amend paragraph [0001] as follows:

This application is a continuation-in-part of Application Serial No. 10/150,553, filed May 17, 2002, entitled "Multi-Channel Bit-Serial Analog-to-Digital Converter with Reduced Channel Circuitry," by David Xiao Dong Yang and William R. Bidermann, now U.S. Patent 6,693,575, issued Feb. 17, 2004, which is a continuation-in-part of U.S. Patent Application No. 09/965,600, filed on September 25, 2001, entitled "Analog-to-Digital Converter with Multiplexed Input Channels," by David Xiao Dong Yang and William R. Bidermann, now U.S. Patent NO. 6,518,909, issued February 11, 2003, which is a continuation of Application Serial No. 09/823,443, filed March 30, 2001, entitled "Multiplexed Multi-Channel Bit Serial Analog-to-Digital Converter," by David Xiao Dong Yang and William R. Bidermann, now U.S. Patent 6,310,571, issued Oct. 30, 2001. The aforementioned patents and patent application are incorporated hereby by reference in their entireties.

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**Amendments to the Drawings**

The attached five sheets of drawings includes changes to Figures 1, 2, 3, 4 and 6. The legend "Prior Art" has been added to each of the figures. The attached sheets, which include Figures 1, 2, 3, 4 and 6, replace the original sheets including the same figures.

Attachment: Replacement Sheets 1-4 and 6/14 (total of 5 sheets).  
Attached at the end.

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